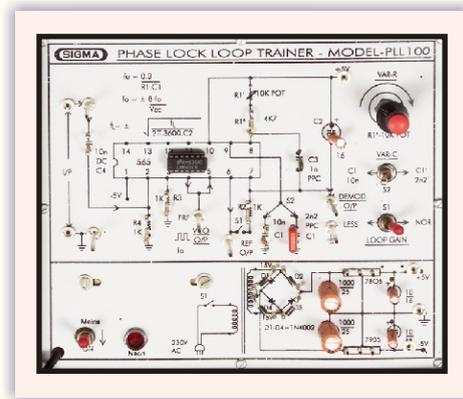




PHASE LOCK LOOP (PLL) TRAINER

MODEL - PLL100

This trainer has been designed with a view to provide practical & experimental knowledge of a general circuit of Phase Lock Loop (PLL) on a single PCB.



SPECIFICATIONS

1. Power supply requirement : 220v AC, 50 Hz.
2. Built in power supply : IC based
3. ICs used : 565
4. Experiments possible:
 1. Study of P.L.L.
 2. Capture Range study.
 3. Locking Range and free running frequency determination.
 4. Frequency modulation by PLL.
 5. FSK Demodulation by PLL.
 6. Frequency Synthesizer by P.L.L.
5. All parts are soldered on single PCB with complete circuit diagram screen-printed.
6. Standard Accessories : A Instructional Manual.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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Dealer:-